

# Embedded Hardware Engineer-Team Lead

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LEONS' INTEGRATIONS | VADODARA

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# Version History

VERSION NO.	DETAILS	PREPARED BY	VERIFIED BY	APPROVED BY	DATE
1.0	Document	Priya Nair	Hardik Mewada	Hardik Mewada	June 2019

# Job Description for Embedded Hardware (T.L) Job Brief

We are looking for a high-performing T.L Embedded Hardware Engineer who will be responsible for Planning of Project & Team, Validation and Approval of Designs using appropriate process, Timely Completion of Task for Teams Managed under him, Create Reports, Lead and facilitate the constructive communications..

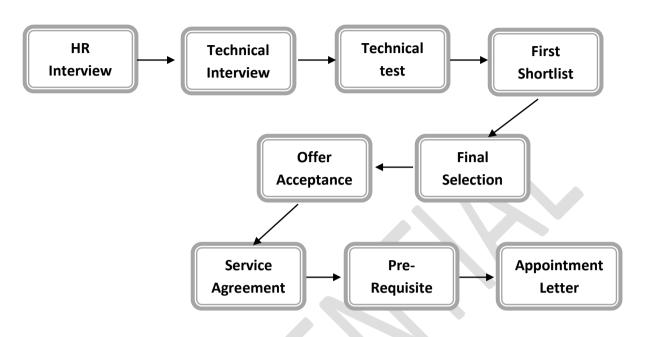
## Role & Responsibility

Role	Your role is to provide direction, instructions and guidance to a team, for the			
Definition	purpose of achieving a certain goal. Their Role is to get the task done by using			
	allocated resources available with them.			
Responsibility	Planning of Project.			
• Planning of Team.				
	<ul> <li>Validation and Approval of Designs using appropriate process.</li> </ul>			
	<ul> <li>Timely Completion of Task for Teams Managed under him.</li> </ul>			
	Create Reports			
	<ul> <li>Lead and facilitate the constructive communications.</li> </ul>			
Task and Activity	Planning of Project:			
	• Sprint Congration			
	<ul> <li>Sprint Generation</li> <li>Task Validation given by Team Members.</li> </ul>			
	<ul> <li>Monitor Team for timely task completion.</li> </ul>			
	Communicate Clear Instructions to Team Members			
	<ul> <li>Manage flow of Day-2-Day operations.</li> </ul>			
<ul> <li>Manage flow of Day-2-Day operations.</li> <li>Ensure deliverables are prepared to satisfy the project requiren cost and schedule</li> </ul>				
accomplishment, issues and status				
<ul> <li><u>Planning of Team</u></li> <li>Allocation of Task.</li> </ul>				
			<ul> <li>Provide Training to Team.</li> </ul>	
	<ul> <li>Distribute work as per the strength and qualities of the engineers</li> </ul>			
	Keep the Team Focused on Task Completion.			
	Validation and Design Approvals using appropriate process			
	<ul> <li>Monitor the design and development phase for the task allocated</li> </ul>			
	to each team member.			
	<ul> <li>Approve the design after proper validation and documentation</li> </ul>			
	provided by the team members.			
	<ul> <li>Documentation of design specifications and implementation</li> </ul>			
	details. Lead design reviews. Upholding of hardware design			
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	<ul> <li>standards and methodologies</li> <li>Realisation of complex system requirements to provide robust and reliable hardware solutions.</li> <li>Responsibility for the complete implementation of detailed technical items and an understanding /appreciation of system level items.</li> <li>Monitoring and reporting of progress of development to immediate supervisor and development team.</li> </ul>	
Measurement	# of successful completion of Projects/ Tasks	
Matrices	Value Creation of the Product     Desumentation	
	Documentation.	

Growth		Design and Development team		
Scale	Grade A	Director -Technical		
	Grade B	VP-Design and Development		
	Grade B	Grade B General Manager-Design		
	Grade C	Grade C Team lead-Project Manager		
	Grade C	Grade C Project Engineer		
	Grade D	Senior Engineer		
	Grade D	Junior Engineer		
	Grade E	Core Design Trainee-Programming/Analog		
		Design/Circuit Design/QC		

## Selection Process



# Documents required at the time of interview

- 1. Updated Resume
- 2. Photograph
- 3. Salary slips of last three months

## Post Offer Acceptance Process

- The selected Candidates who are willing to accept the Service Agreement will have to submit 2 Revenue Stamp Papers of value 100 Rupees purchased in the name of the candidate and his surety.
- The selected Candidates (freshers) will have to submit their original documents (LC / marksheet / Diploma or Degree Certificates) as well as three bond cheques in the name of the company as part of two years of service agreement.
- For experienced three cheques submission in the name of the company as part of two years of service agreement.
- The marksheet and cheques submitted will be returned to the candidate after completion of two years.

- Each candidate will be required to complete certain Pre-Requisite training –for fresher's before receiving the Appointment Letter.
- Complete Support in terms of Theoretical and Practical Sessions will be provided by the Team for all candidates during the pre-requisite training period.
- It is not necessary that two candidates selected for the same profile be given the same starting package. The Package will depend completely on your skill set and dedication towards the Prerequisite training.

## Documents required at the time of joining

- ✓ Degree/ Diploma Mark sheets original as well as copy
- ✓ 3 Cheques submitted in the name of the company
- Id and address proof copy
- ✓ Reliving letter, if applicable
- ✓ last three month's salary slips, for experienced
- ✓ Joining form to be filled for company records
- ✓ Declaration sheet to be signed by the surety
- ✓ Undertaking letter signed by the candidate

## **Retention Policy**

- Understanding and valuing the efforts of all team members towards the making and growing of this company, the team members are rewarded with a retention bonus who complete 3 years within the company at a stretch.
- We herewith commit to award each employee a lumpsum retention bonus of the amount equivalent to the sum of average annual salaries drawn by the employee during the first, second and the third year at the completion of 36 months of active service.

## Pre-Requisite

- C Programming
   (INCLUDING File Handling, Pointers, Link List, Binary Tree, Graphic Programming)
- R / L / C / RL / RC / LC / RLC networks & Filter Design.
- Transistor (as an amplifier and as a switch), SCR / Triac / DIAC / Mosfets.
- PCB Designs (single and Dual layer)
- Power Supply design
  - (INCLUDING SMPS / DC DC in Constant Voltage and Constant Current configurations)
- Magnetics of Inductors and Transformers.
- 8085 & 8086 (minimum system design)
- 8051 Microcontroller
  - (INCLUDING Architecture, GPIO / Timer / Counter / Interrupt programming in C & Assembly)
  - (INCLUDING Communication Interfaces like Serial, parallel, I2C, SPI, Bit banding, Power Modes)
  - (INCLUDING Testing on breadboard, PCB design and soldering and Testing)
  - (INCLUDING GLCD, GSM, GPS, 7-Seg, ADC, DAC, EEPROM, Relay, 4x4 keypad, stepper Motor)
- 8 Bit MCU (AVR)

## Training Program

Under Professional Training Program on Hardware the company expects you to acquire thorough knowledge of the following modules:-

#### 1. Basic Theory

Basic Concepts Of Electricity	DC Network Analysis
Ohm's Law	<ul> <li>Magnetism and Electromagnetism</li> </ul>
Series And Parallel Circuits	Coupled Circuits
• Passive Elements: Diode, Light-emitting diode (LED)	<ul> <li>Network Theorems : DC Analaysis</li> </ul>
RC and L/R Time Constants	<ul> <li>Physics Of Conductors And Insulators</li> </ul>
<ul> <li>Divider Circuits And Kirchhoff's Laws</li> </ul>	<ul> <li>Passive Elements: Resistor, Capacitors, Capacitor</li> </ul>
Series-parallel Combination Circuits	(component)
-	Inductors

## 2. Basic AC Theory

Root Mean Square, Average electrical power	Practical's
Transformers	Introduction
<ul> <li>Reactance and Impedance Inductive</li> </ul>	Basic Concepts and Test Equipment
Power Factor	DC Circuits, AC Circuits
Reactance And Impedance Capacitive	555 Timer Circuits
• Reactance And Impedance R, L, And C	
• Filters	

#### 3. Regulator & SMPS

1. Linear regulator	2. SMPS Design	3. DC To DC Regulator
Zener diode	• Fly back - 9W, 18W	5V DC Regulated Supply
<ul> <li>Low dropout (LDO) regulator</li> </ul>		12V DC Regulated Supply
<ul> <li>Band gap voltage reference</li> </ul>		<ul> <li>Variable 3V to 6V Regulated</li> </ul>
		Supply

## 4. Semiconductor Related

Solid-state Device	Junction Field-effect Transistors
Diodes and Rectifiers	Amplifiers and Active Devices
Bipolar Junction Transistors	Active Filters
Thyristors and Operational Amplifiers	MOS Field-effect Transistors

## 5. Digital Theory

•	Numeration Systems	•	Switches
•	Binary Arithmetic	•	Electromechanical Relays
•	Logic Gates	•	Combinational Logic Functions
•	Multivibrators	•	Sequential Circuits
•	Ladder Logic	•	Shift Registers
•	Boolean Algebra and Karnaugh Mapping		Digital-Analog Conversion

## 6. Oscillator - Related

• 555 timer IC	Latch
Multivibrator	Relaxation oscillator
• Flip–flop	Schmitt trigger

#### 7. Amplifiers

<u>1. Amplif</u>	ier classes	2. Crossover distortion
Class A, Class B and AB, Class	Differential Amplifier , Common	3. Switching amplifier
C, Class D	Emiiter Amplifier	
Common Collector	Push Pull	

#### 8. Control Systems/Transducers/Conversion

<ul> <li>PID control using Hardware and Software.</li> <li>System Stability consideration aspects.</li> </ul>	<ul> <li>Understanding and Implementation of Transducers and Sensors.</li> </ul>
	Conversion from Voltage to Current Source and Vice Versa.

# Company Profile

Leons Integrations Pvt. Ltd. is a complete automation developer unit based in Vadodara, India. Within just 15 years of existence, we've generated a reputation in the industry as a Design House and Solutions Provider. We provide tailor-made solutions in Electronic and Mechanical Hardware as well as Software for each of our esteemed clients across the global.

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We create Value Preposition for our customers by:

- ✓ Flexibility in the Design
- ✓ Seamlessly understanding the clients' needs and limitations
- ✓ Using Agile Methodology at every stage to make every interaction Transparent at every stage
- ✓ Reliability and Continuous Improvement
- ✓ Simple and Durable Solutions

## Our Products



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